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Questions and Answers on VLSI Design & Technology Multiple Choice Questions and Answers By Sasmita January 13, 2017 1) The utilization of CAD tools for drawing timing waveform diagram and transforming it into a network of logic gates is known as _____.

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With Answers
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This may mean dedicated conversion functions are needed to convert objects from one type to another. The choice of which data types to use should be considered wisely, especially ...

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(Verilog interview questions that is most commonly asked) ... The choice of which data types to use should be considered wisely, especially enumerated (abstract) data types. This will make models easier to write, clearer to read and avoid unnecessary conversion functions that

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can clutter the code. ... When multiple defparams for a single ...

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Multiple Choice Questions and Answers on VLSI Design & Technology Multiple Choice Questions and Answers By Sasmita January 13, 2017 1) The

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utilization of CAD tools for drawing timing waveform diagram and transforming it into a network of logic gates is known as _____.

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